IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

INFORMATION **DISCLOSURE**

STATEMENT

Examiner: To Be Assigned

In re PATENT APPLICATION of

Applicant(s) : Motoki KOBAYASHI et al.

: To Be Assigned Appln. No.

: September 30, 2003 Filed

: SEMICONDUCTOR DEVICE AND For

SEMICONDUCTOR DEVICE FABRICATION METHOD

Atty. Dkt. : FUJI 137

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of four Japanese patent publications. Any relevance of the publications can be gleaned from the attached English-language Abstracts. The publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted documents is respectfully requested.

September 30, 2003

RHB:tz

Respectfully submitted,

Robert H. Berdo, Jr. (Reg. No. 38,075)

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FORM PTO-1449				Atty Docket	Applicati	Application No.		
INFORMATION DISCLOSURE STATEMENT				FUJI 137	To Be A	To Be Assigned		
				Applicant				
				Motoki KOBAYASHI and Fumio ICHIKAWA				
				Filing Date		Group Unit To Be Assigned		
				September 30, 2003	To Be A			
			U.S. 1	PATENT DOCUMENTS				
Examiner Initial		Document Number	Date	Name	Class	Sub- Class	Filing Date	
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	AN	09-270515	10/14/97	Japan			Abst	
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